

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: William R. Wheeler et al.

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Commissioner for Patents

Technology Center 2100

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Attention: Official Draftsman

## TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed four (4) sheets of formal drawings for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

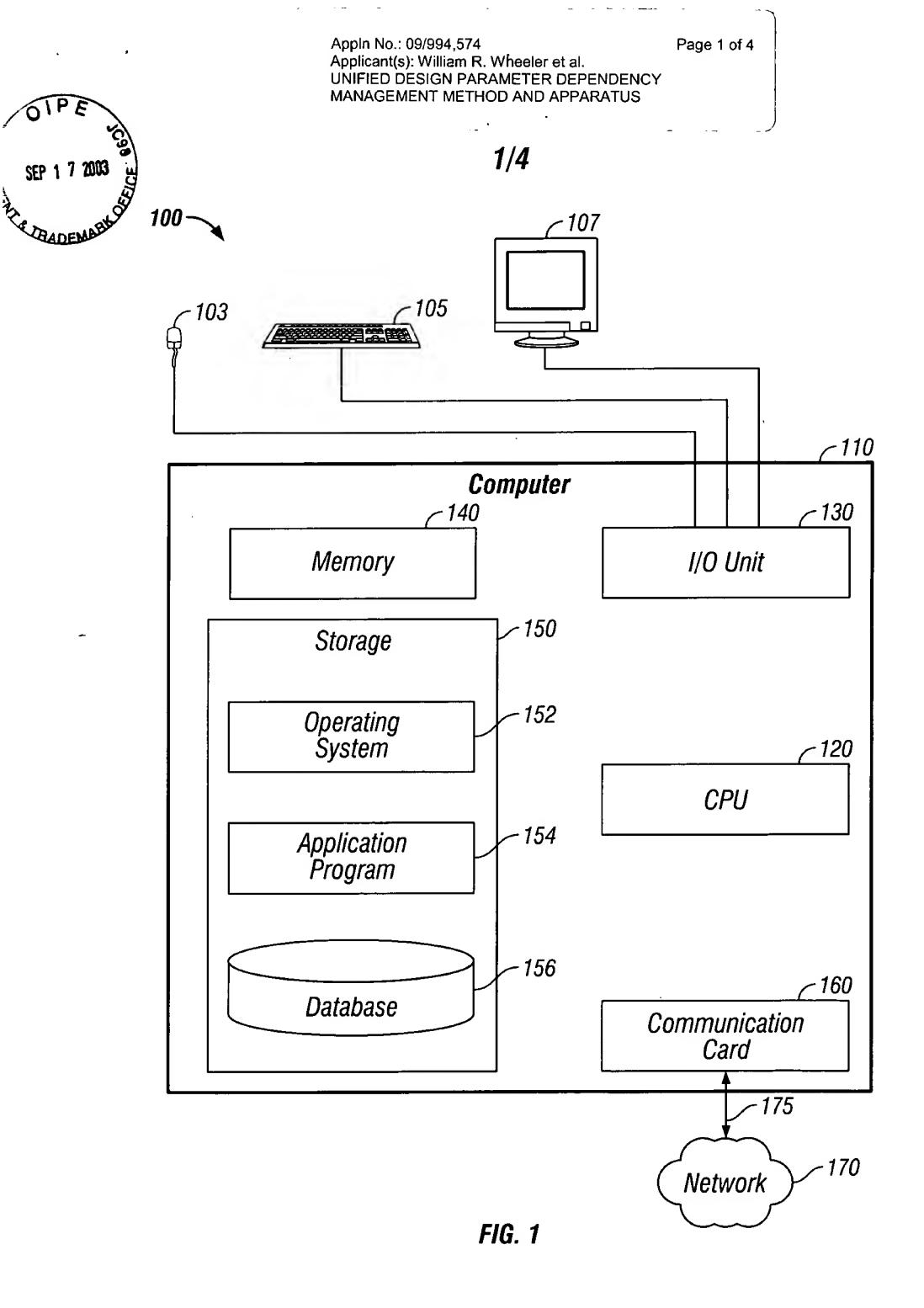
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200-

Upper Data Field		Lower Data Field		Status		Parity	
31	16	15	6	5	3	2	0
240	240			220-	,	210-	<del>,      </del>

FIG. 2

*300* ~

Data_Word_Range	31:0
Upper_Data_Range	31:16
Lower_Data_Range	15:6
Status_Range	5:3
Parity_Range	2:0
Data_Word_Width	32
Upper_Data_Width	16
Lower_Data_Width	10
Status_Width	3
Parity_Width	3

FIG. 3

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reg [LOWER\_DATA\_WIDTH-1:0] lower\_data\_field;
reg [STATUS\_WIDTH-1:0] status;
reg [PARITY\_WIDTH-1:0] parity
upper\_data\_field[UPPER\_DATA\_WIDTH-1:0] = data\_word[UPPER\_DATA\_RANGE];
lower\_data\_field[LOWER\_DATA\_WIDTH-1:0] = data\_word[LOWER\_DATA\_RANGE]; 'ATUS\_WIDTH-1:0] = data\_word[STATUS\_RANGE];  $RITY\_WIDTH-1:0J = data\_word[PARITY\_RANGE];$ reg [DATA\_WORD\_WIDTH-1:0] data\_word; reg [UPPER\_DATA\_WIDTH-1:0] upper\_data\_field; 4\_WORD\_WIDTH-1:0] data\_word; status/ST, parity[PAI

FIG. 4A

reg [31:0] data\_word;
reg [15:0] upper\_data\_field;
reg [9:0] lower\_data\_field;
reg [2:0] status;
reg [2:0] parity;
upper\_data\_field[15:0] = data\_word[31:16];
lower\_data\_field[9:0] = data\_word[15:6];
status[2:0] = data\_word[2:0];

FIG. 4B

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510

Define a Signal Parameter Value

520

Maintain the Defined Signal Parameter Value in a Central Database

530

Use the Defined Signal Parameter that is Maintained in the Central Database in Computer Code for a Logic Design

FIG. 5